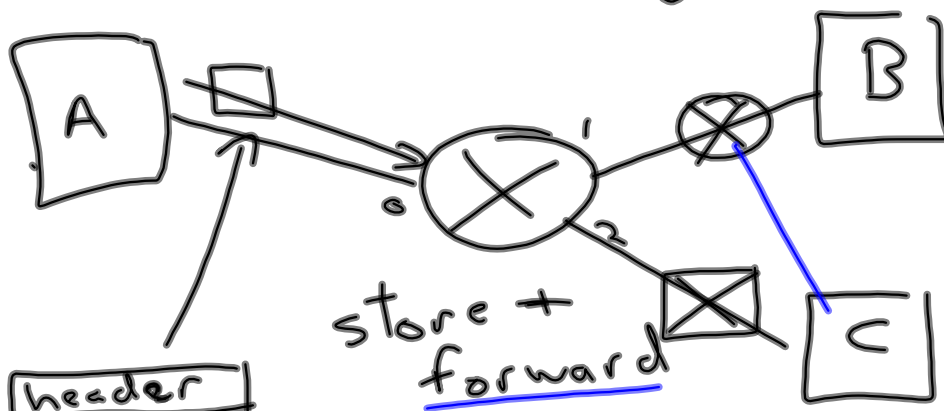


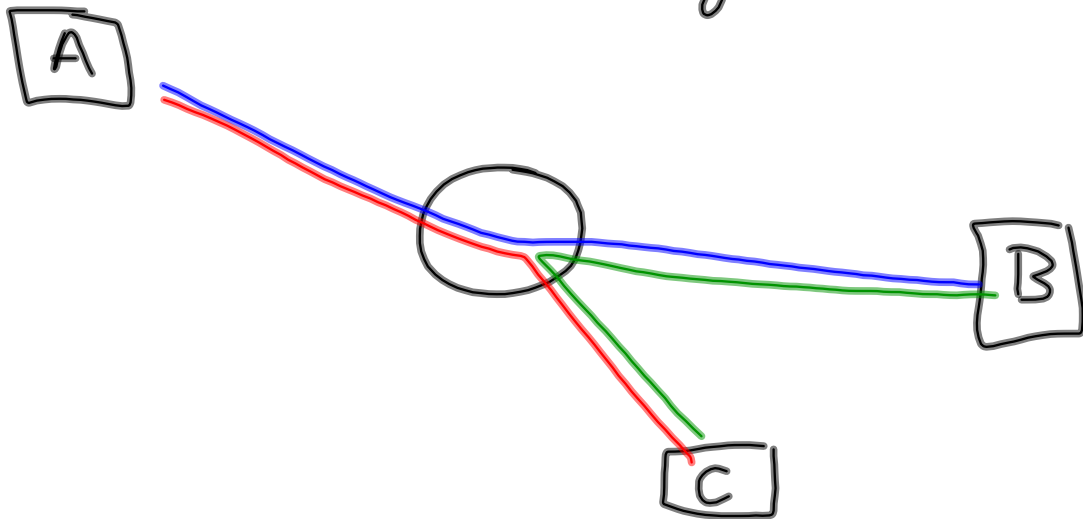
Packet Switching



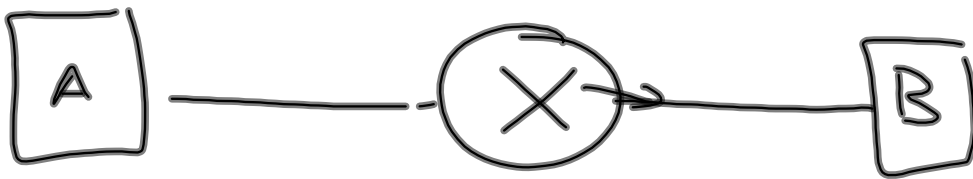
header	output
B	1
C	2
A	0

built by routing

Circuit Switching



Delay



processing - examine header, forward

queueing - waiting in line (at router)

transmission - onto ^{output} "wire"

propagation - going across "wire"