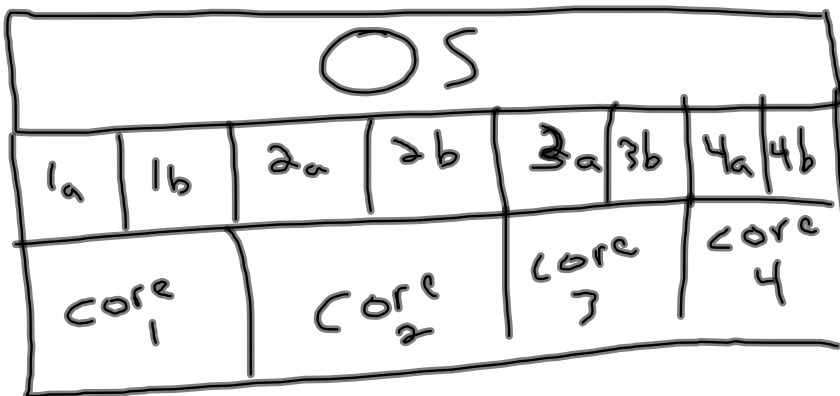


Amdahl's Law

$$= \frac{\text{Exec Time Affected}}{\text{Amt of improvement}} + \text{Exec time un affected}$$
$$= \frac{\text{Parallel code}}{\# \text{ of processors}} + \text{serial code}$$

Hardware Multi-threading "Hyper Threading"



Instr/Data

SISD

MIMD

SIMD

~~MISD~~

SPMD - multi-thread

↑
program

OpenMP

