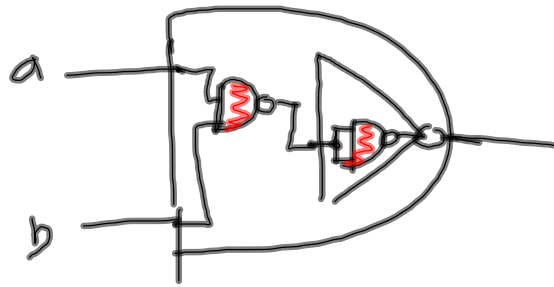
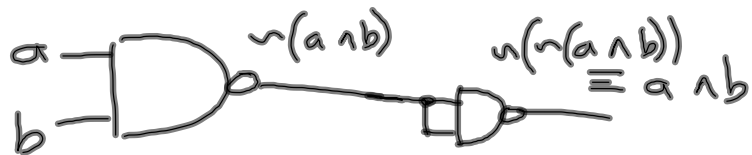


NOT w/ NAND



AND w/ NAND



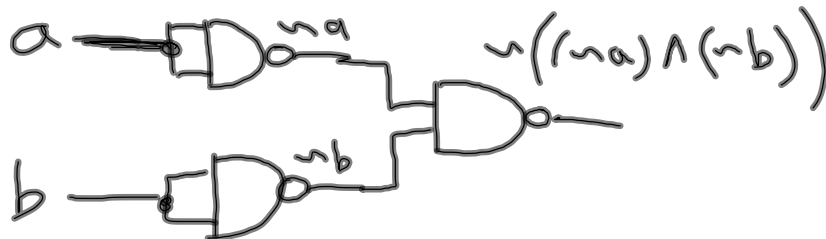
OR $a \vee b$

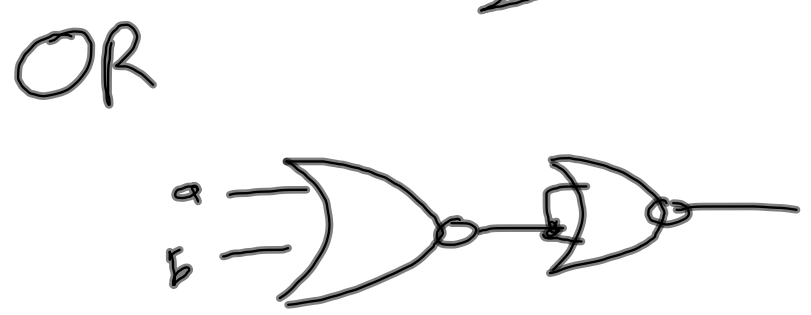
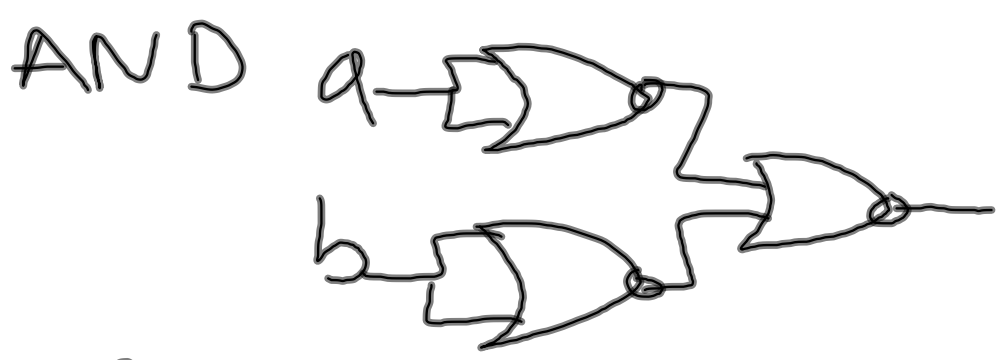
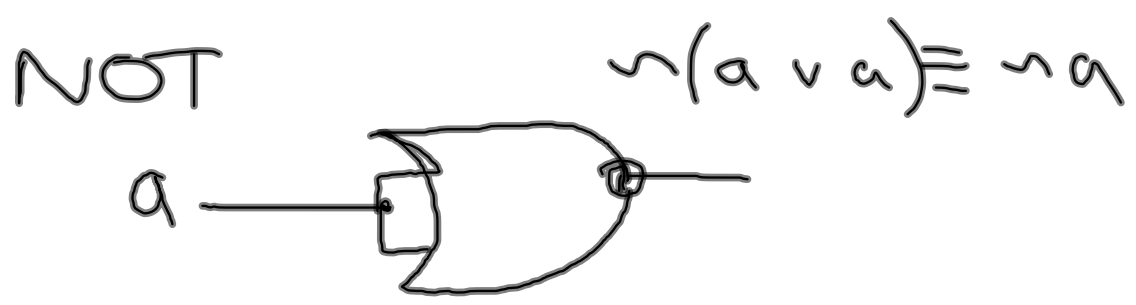
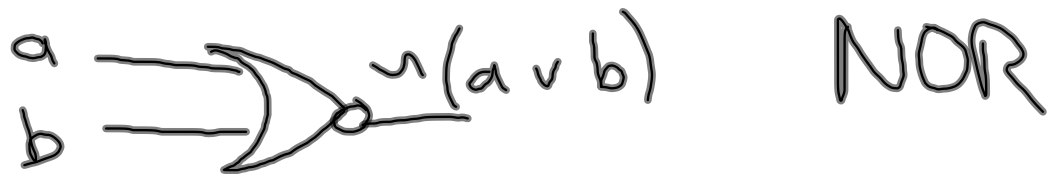
$$\equiv \neg(\neg(a \vee b))$$

$$\equiv \neg(\neg a \wedge \neg b)$$

DeMorgan's Law

$$\neg(a \vee b) \equiv \neg a \wedge \neg b$$





a	b	c	
-	-	-	0
-	-	0	0
-	0	0	0
0	-	-	0
0	0	-	0
0	-	0	0
0	0	0	0

logicSim

